



RESPONSE UNDER 37 C.F.R. §1.116  
-EXPEDITED PROCEDURE-  
EXAMINING GROUP 2836

ATTORNEY'S DOCKET NO: S1022/7556

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: S. Pontarollo  
Serial No: 08/653,958  
Filed: May 22, 1996  
For: CLAMP  
Examiner: Leja, R.  
Art Unit: 2836

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

The undersigned hereby certifies that this document is being placed in the United States mail with first-class postage attached, addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231, on May 10, 1999.

  
Dana M. DeCorso

BOX AF  
Assistant Commissioner for Patents  
Washington, D.C. 20231

RECEIVED

MAY 20 1999

TECHNOLOGY CENTER 2800

Sir:

AMENDMENT AFTER FINAL UNDER RULE 1.116

In response to the Final Office Action mailed December 8, 1998, Applicant respectfully requests reconsideration. To further the prosecution of this application, Applicant has amended claims, and submits the following remarks.

IN THE CLAIMS

1. (Thrice Amended) A device for protecting a circuit against voltage surges, comprising:

[a] an MOS transistor of a first type directly connected to first and second supply terminals by its source and its drain, respectively;

[a] an MOS transistor of a second type directly connected between the second supply terminal and the gate of the transistor of the first type by its source and its drain, respectively;  
and